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FIG. 1

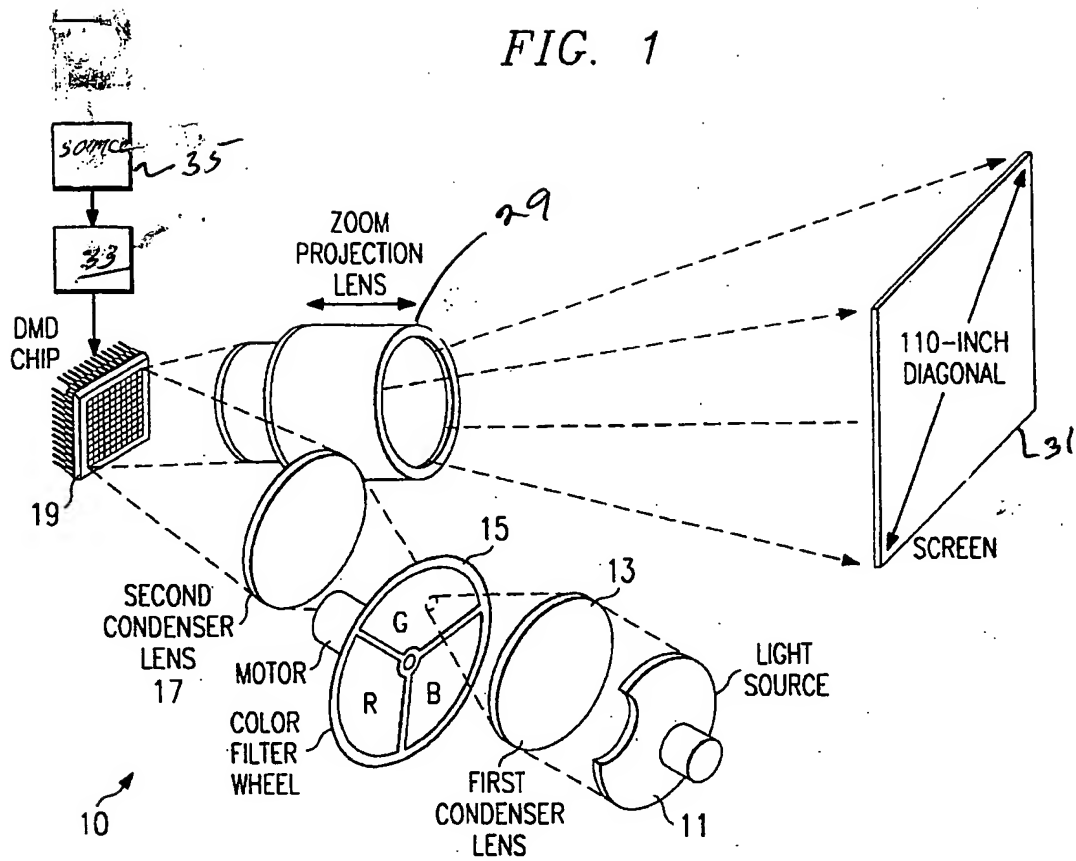
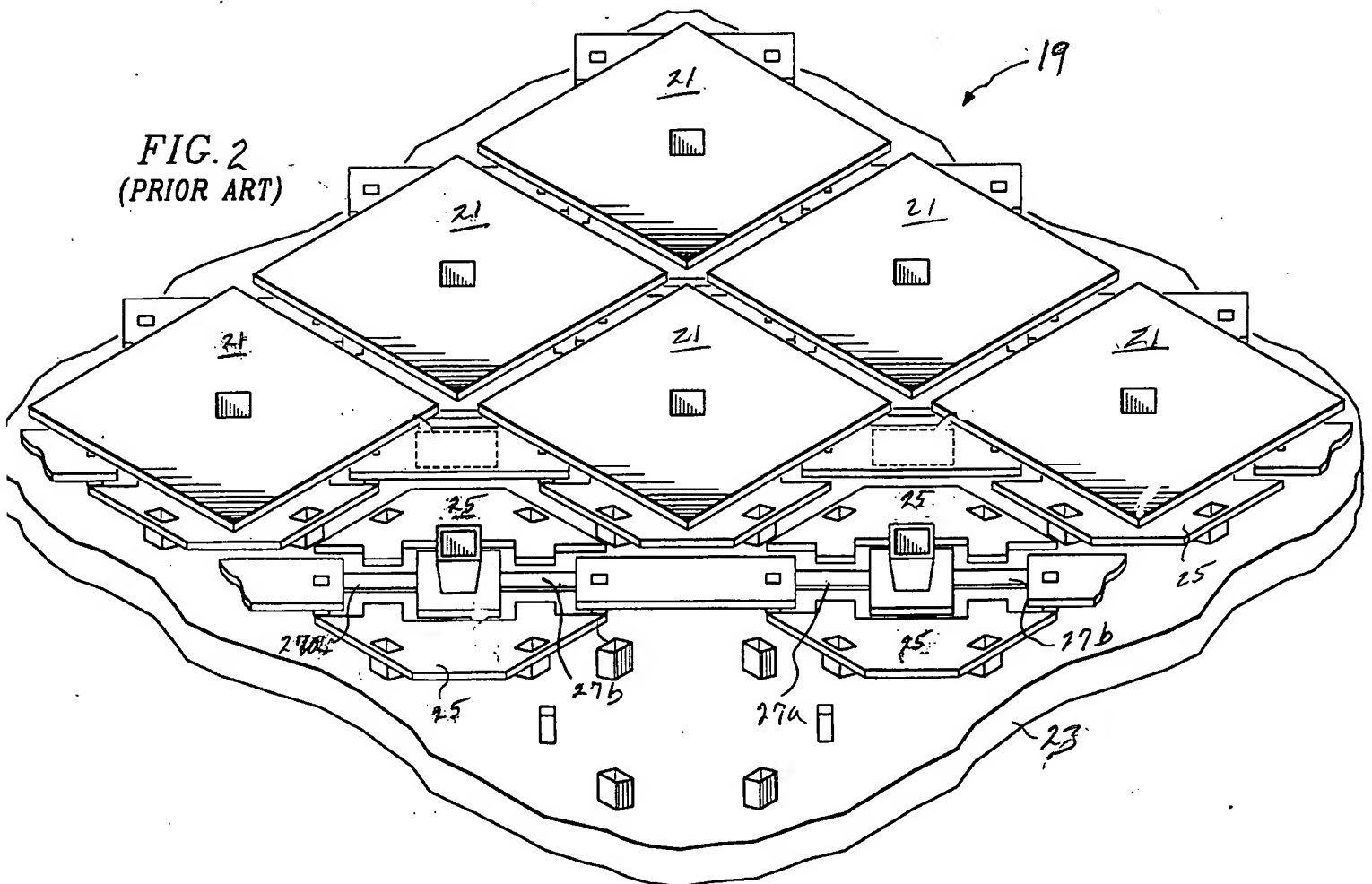


FIG. 2
(PRIOR ART)



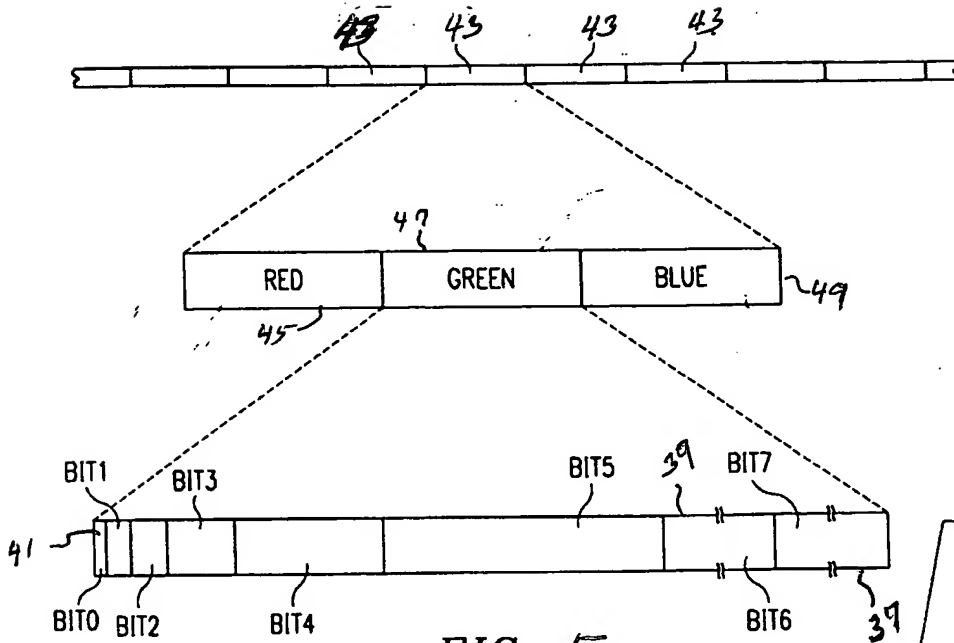


FIG. 5
(PRIOR ART)

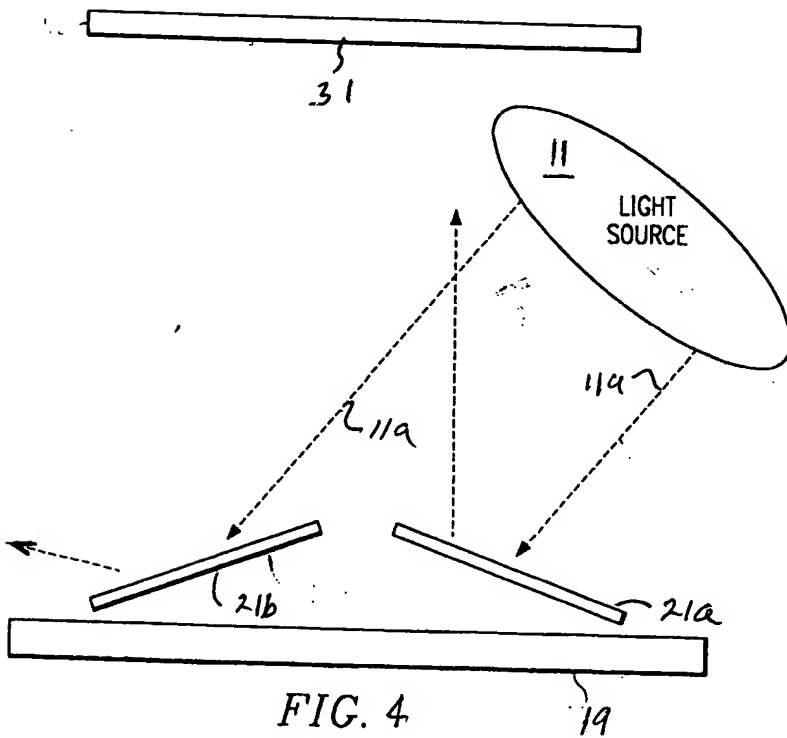
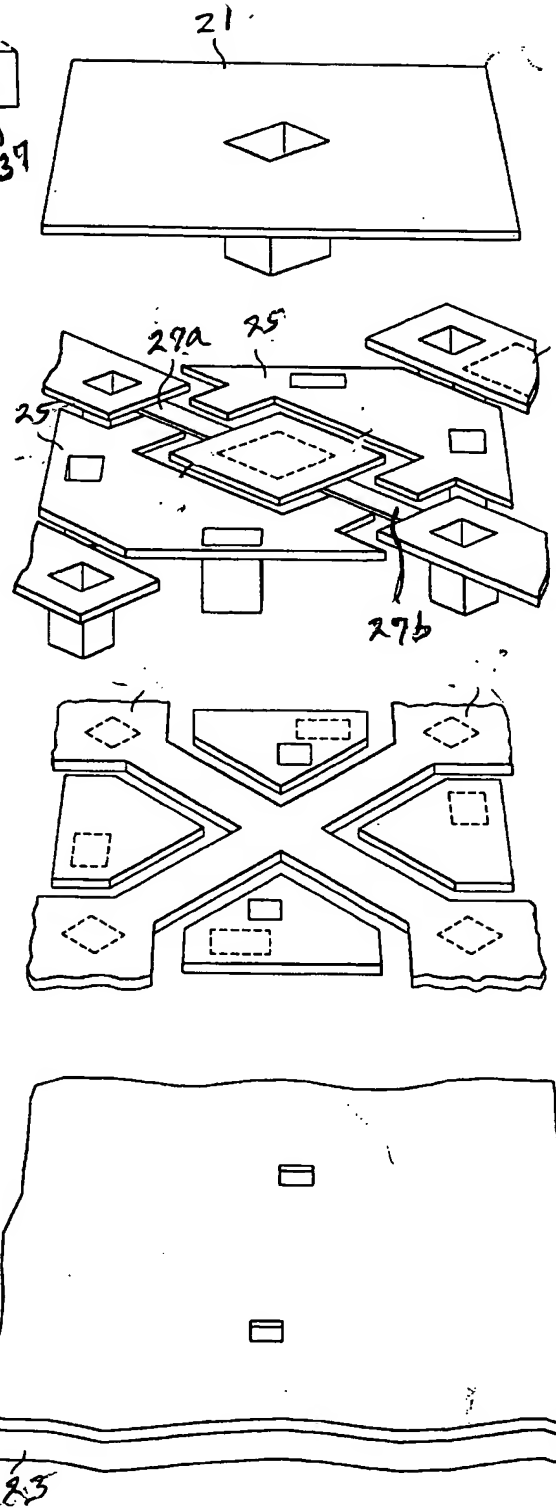


FIG. 4
(PRIOR ART)

FIG 3



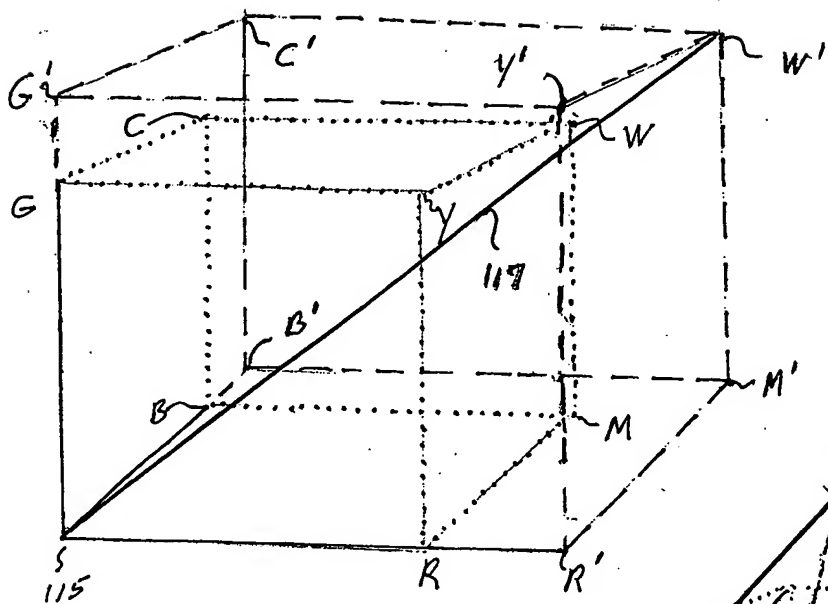


FIG 10A

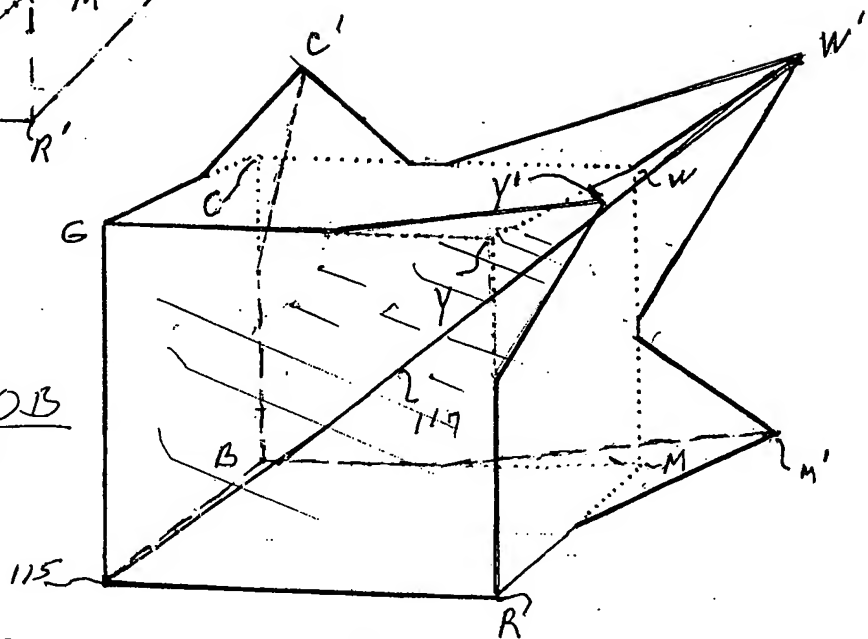


FIG 10B

PRIOR ART

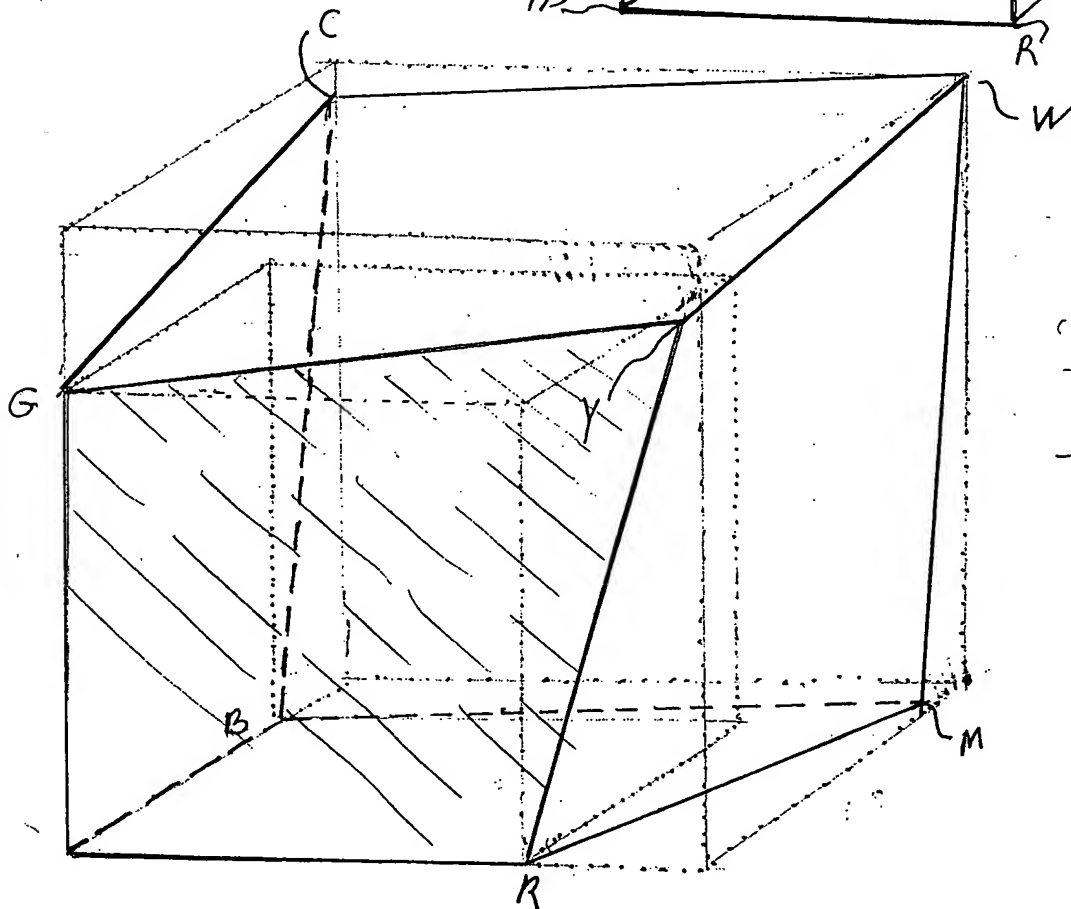


FIG 12

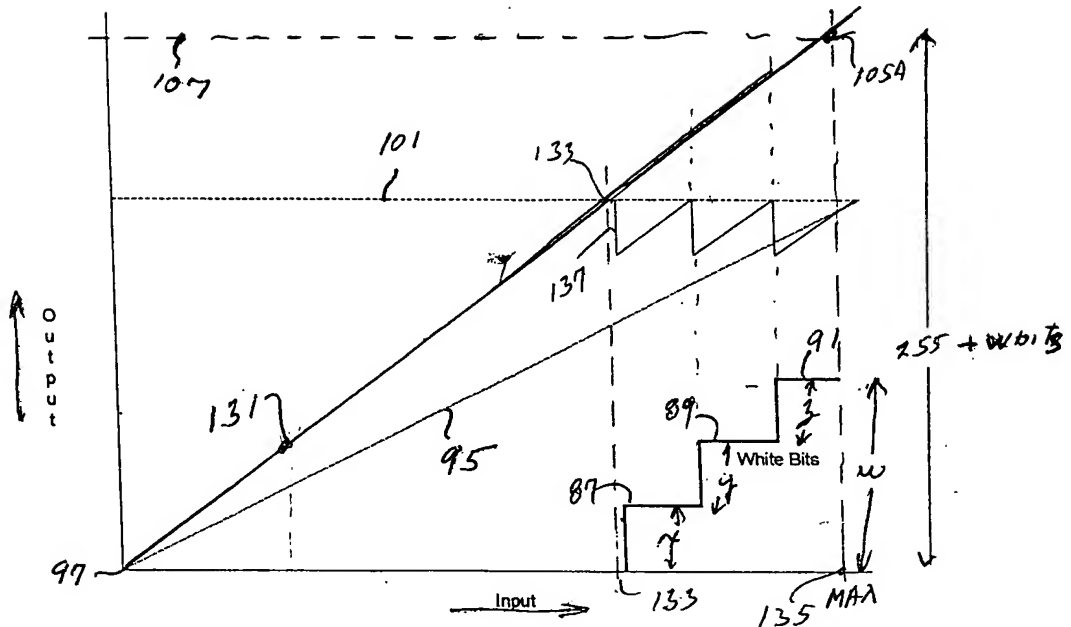


FIG 11

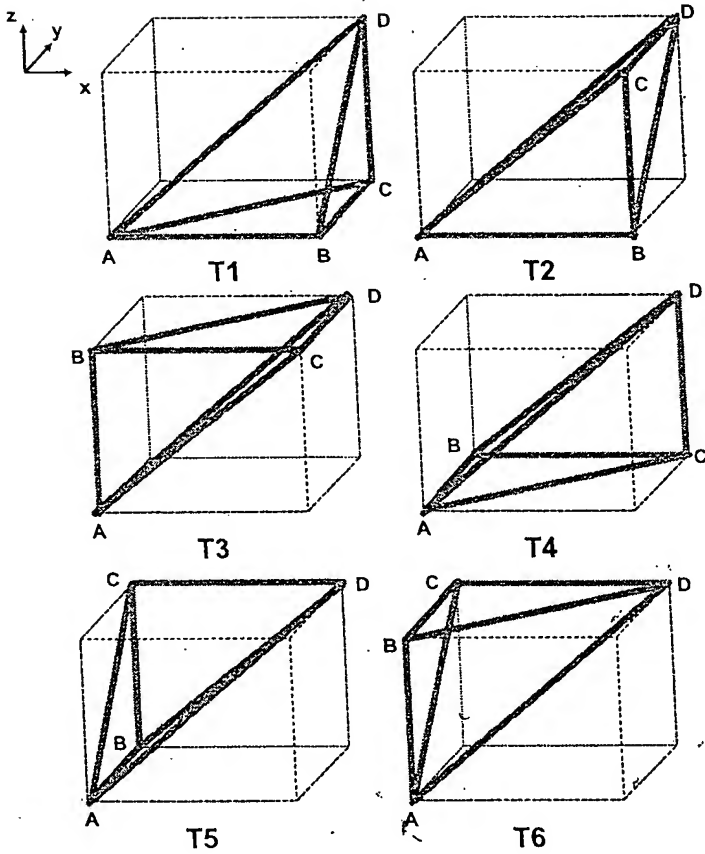


FIG 14

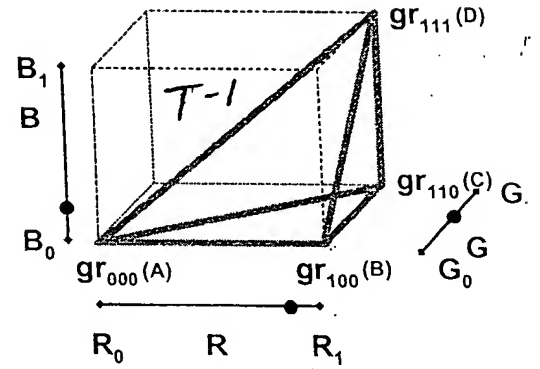


FIG 15

Output: 3 gain factors (R,G,B) & spoke/white/secondary bits to be turned on.

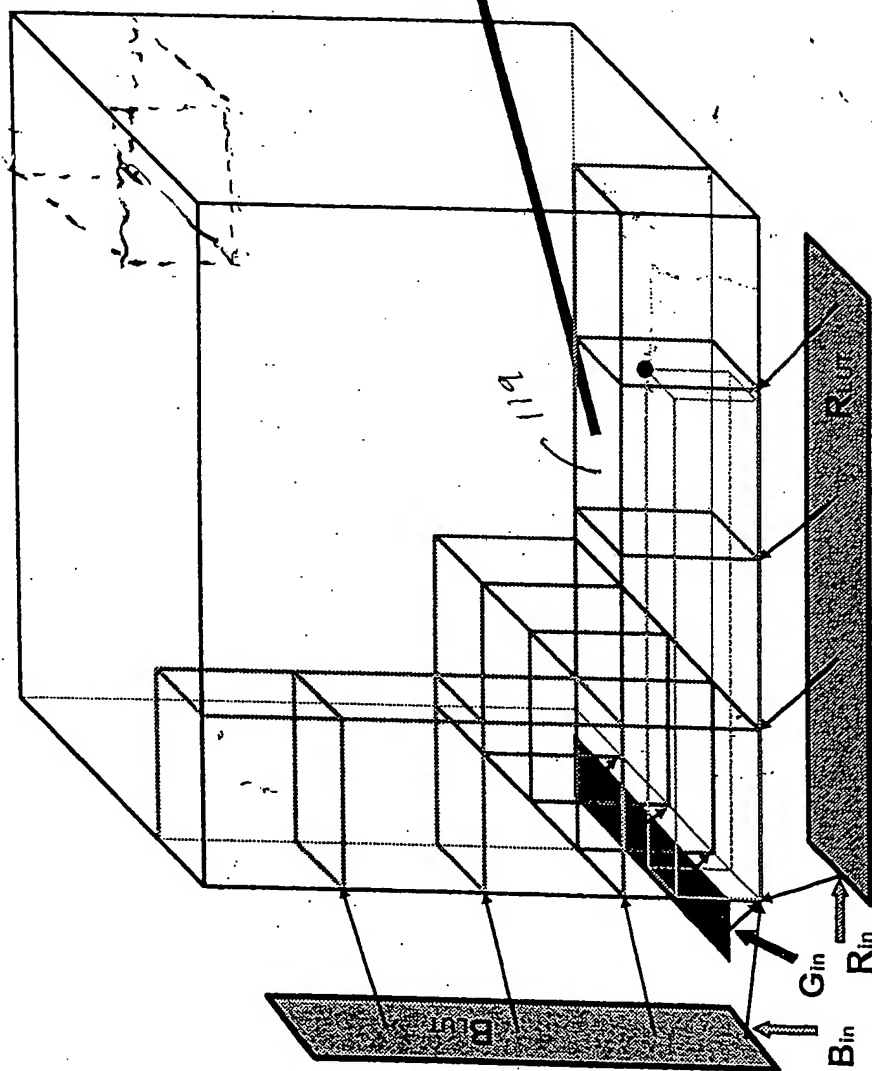


FIG 13

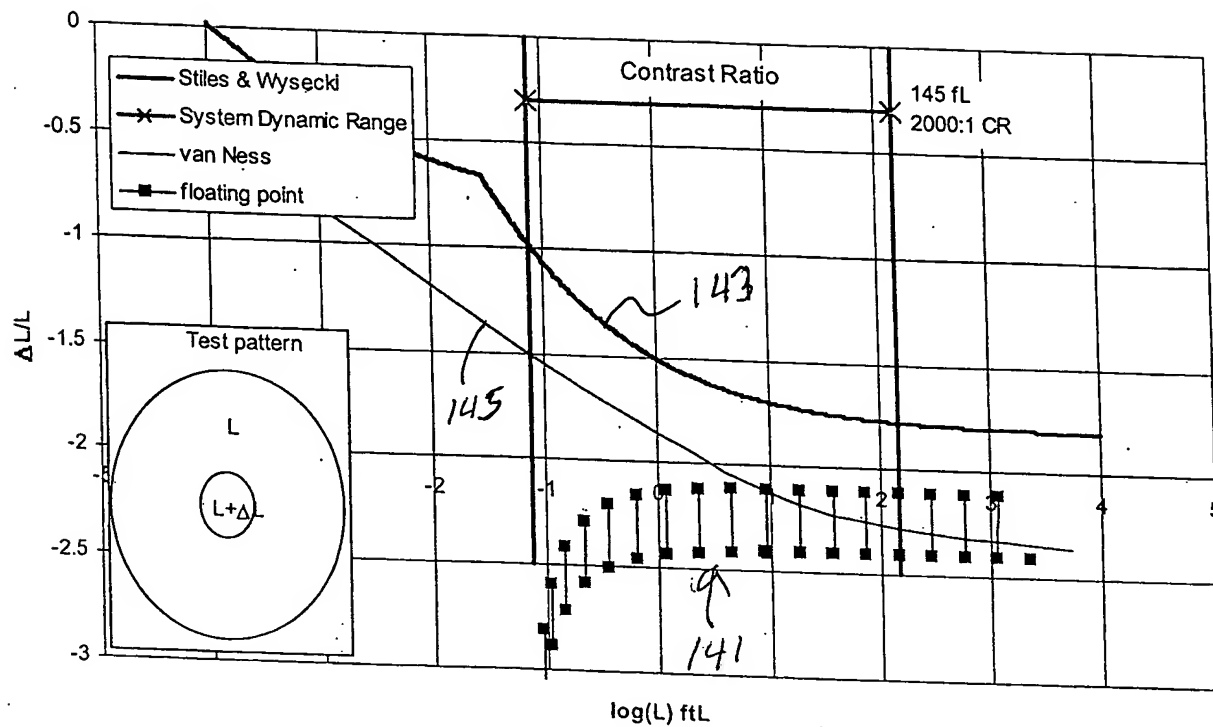


FIG 16

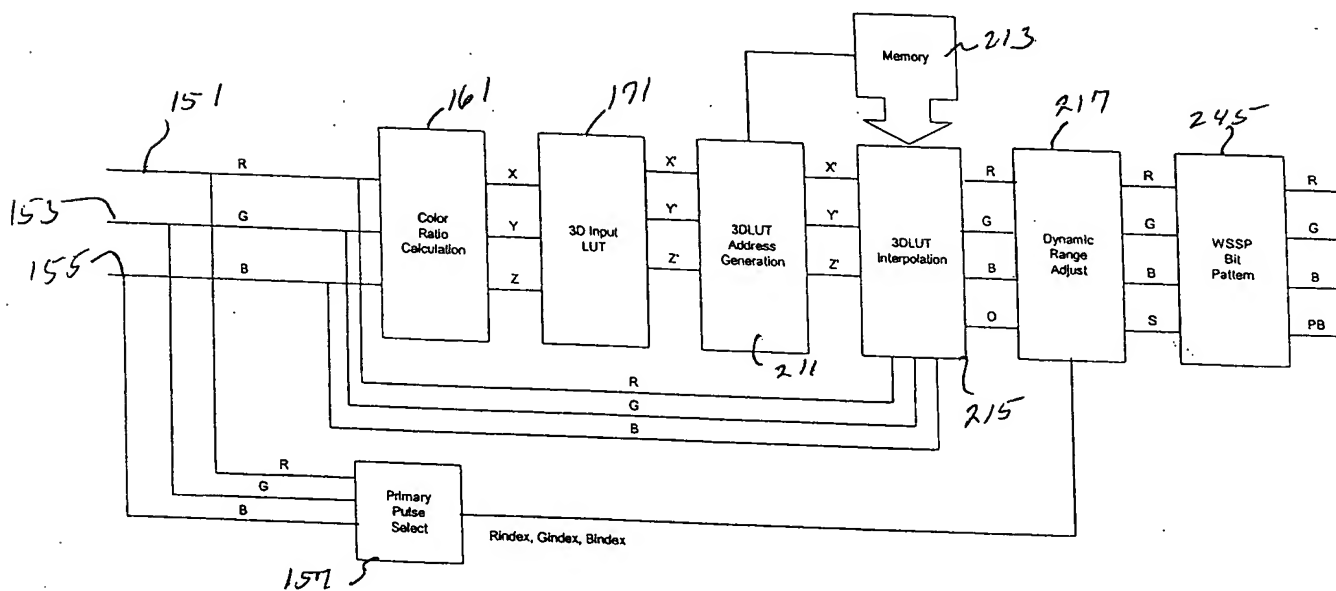


FIG 17

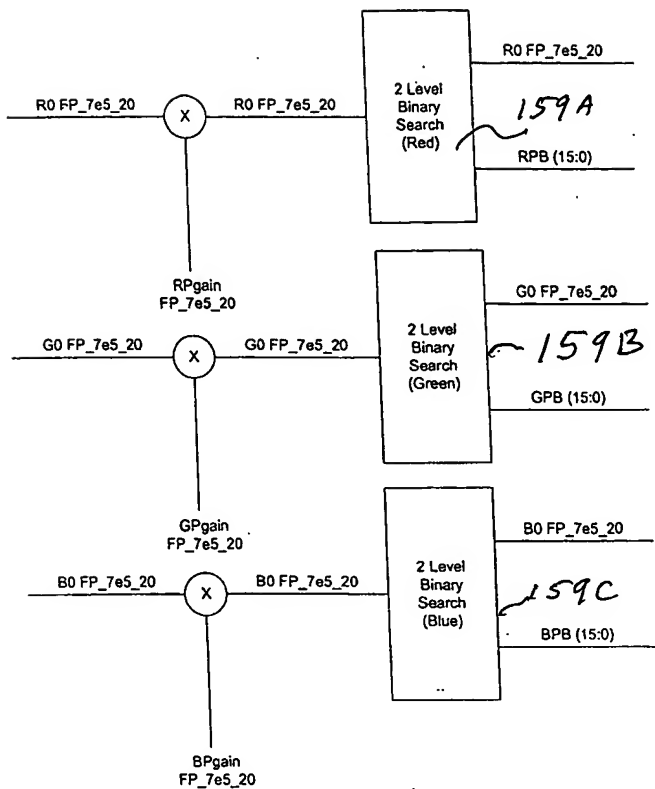


FIG 18

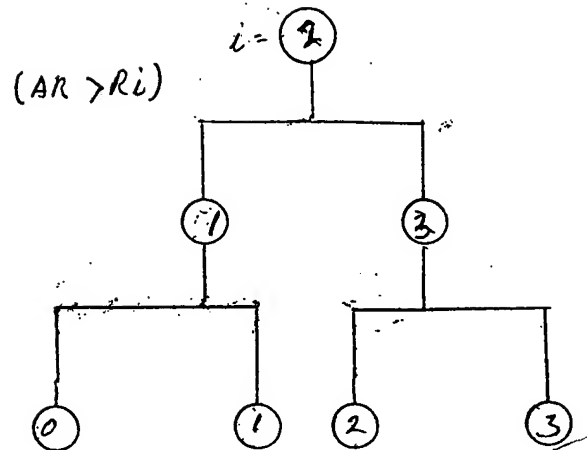


FIG 19

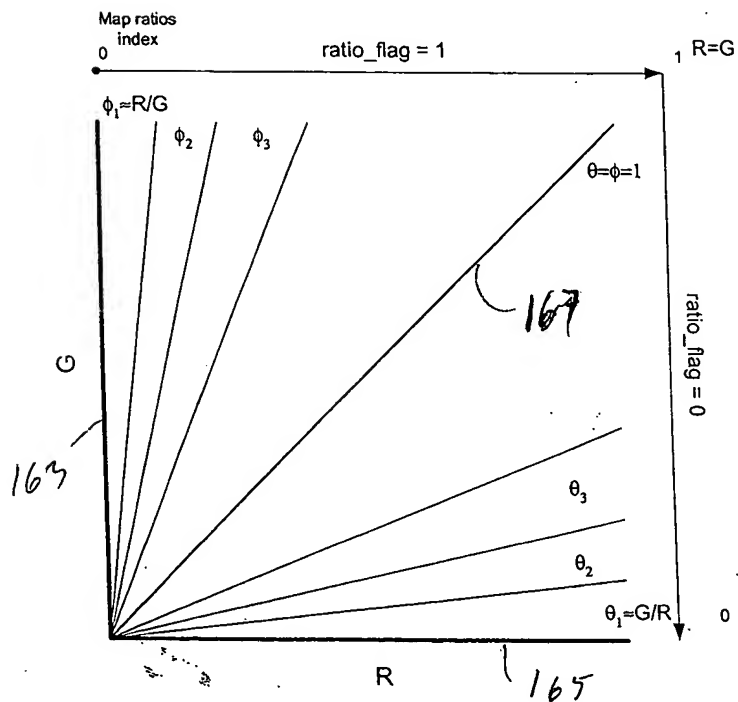


FIG 20

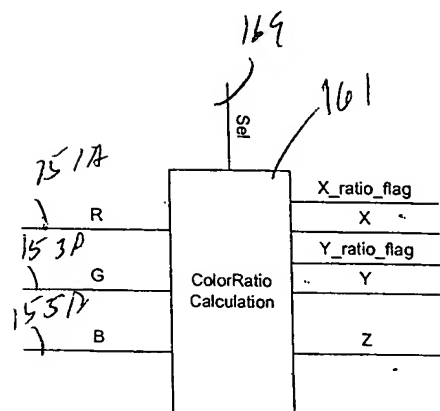
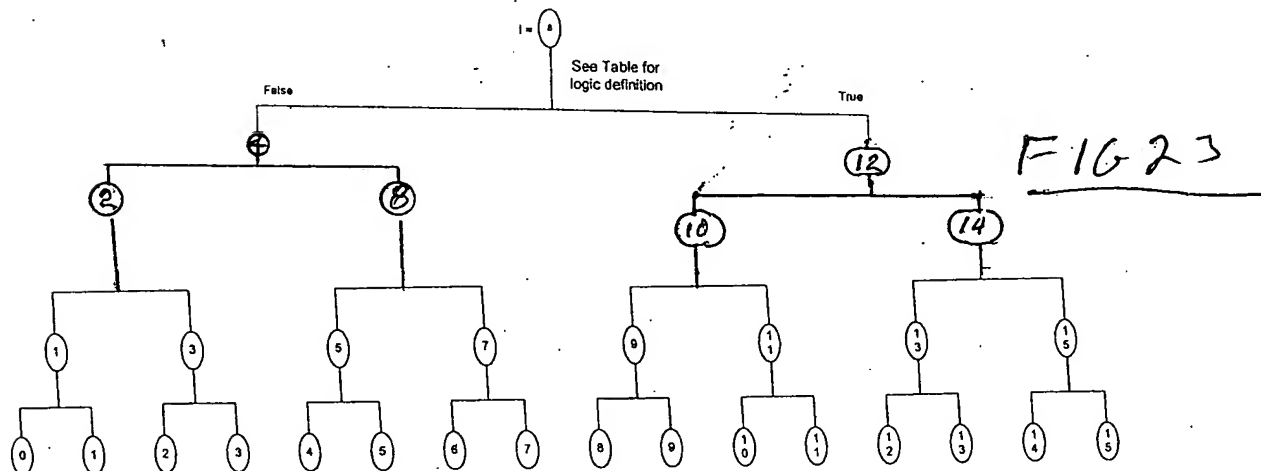
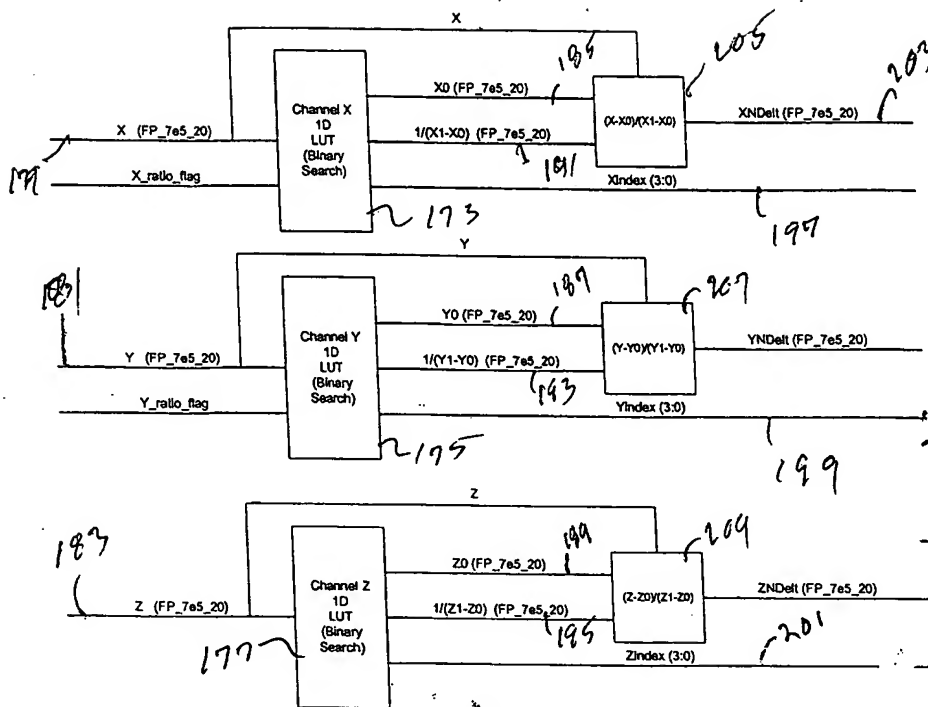


FIG 21



	0.816	1.816	2.816	3.816	4.816	0.817	1.817	2.817	3.817	4.817	0.818	1.818	2.818	3.818	4.818	0.819
Z15	0.768															4.767
Z14	4.716															3.716
Z13	3.665															0.563
Z12	2.614															4.511
Z11	1.563															3.460
Z10	0.512															2.409
Z9	4.460															1.358
Z8	3.409															0.307
Z7	2.358															4.255
Z6	1.307															3.204
Z5	0.256															2.153
Z4	4.204															1.102
Z3	3.153															0.051
Z2	2.102															
Z1	1.051															
Z0	0.000	1.000	2.000	3.000	4.000	0.001	1.001	2.001	3.001	4.001	0.002	1.002	2.002	3.002	4.002	0.003
	X0	X1	X2	X3	X4	X5	X6	X7	X8	X9	X10	X11	X12	X13	X14	X15

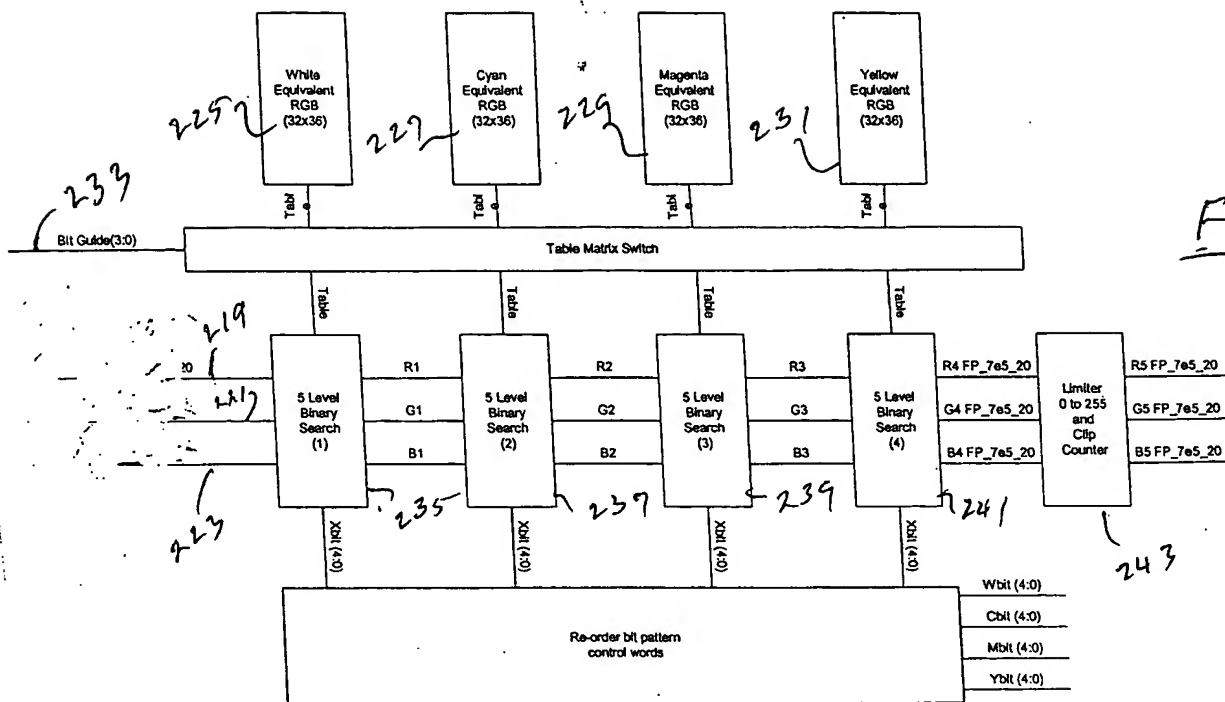
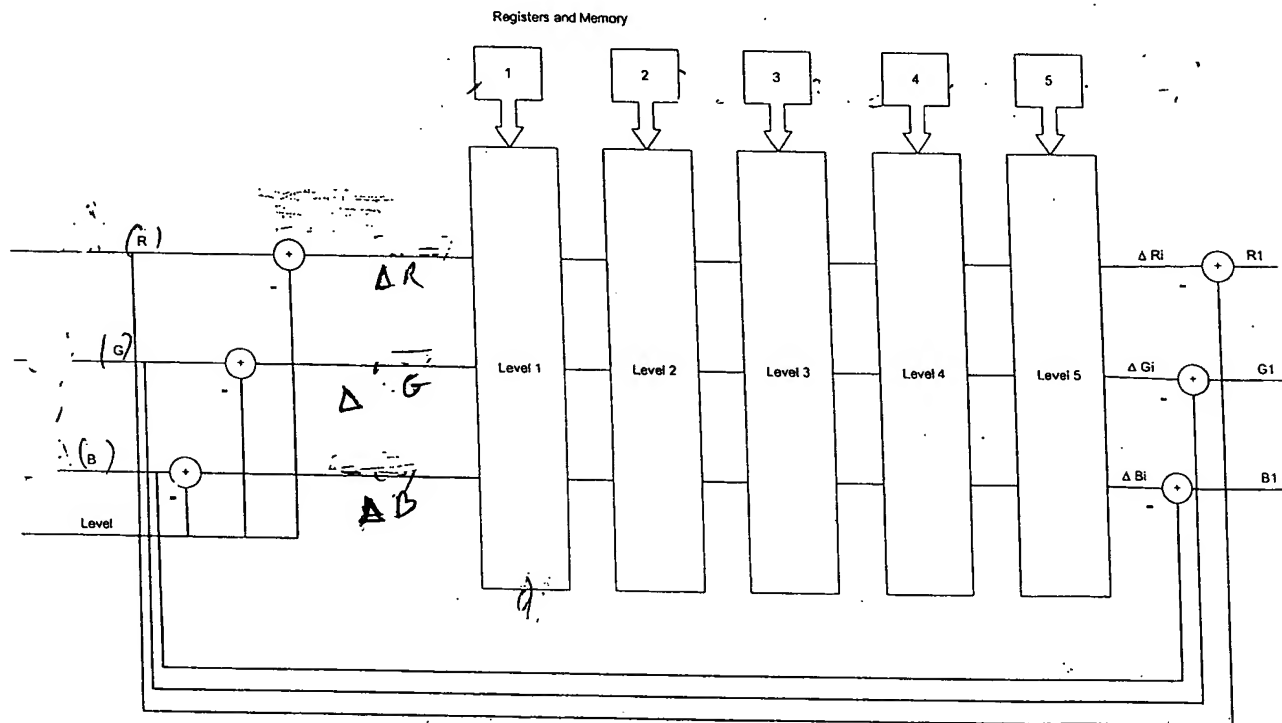
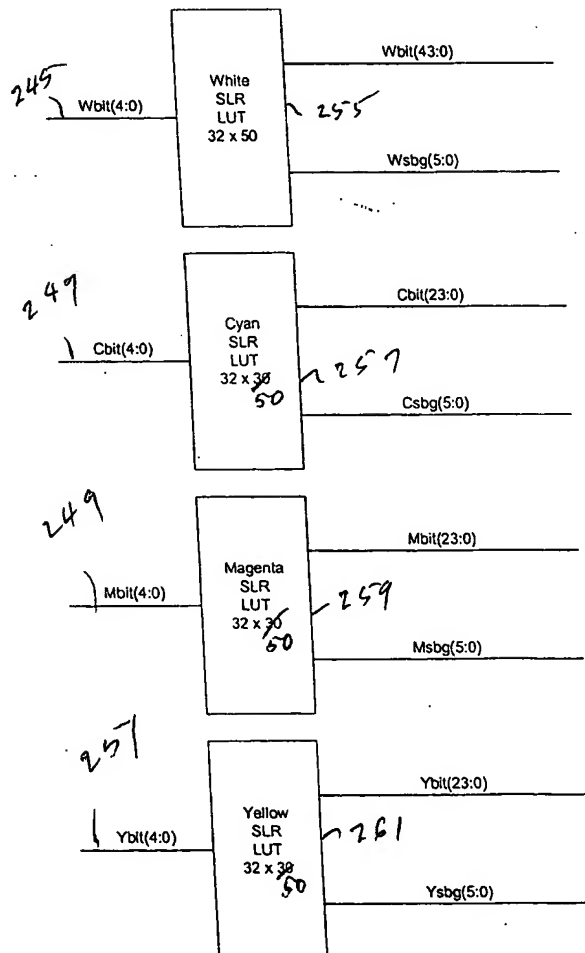
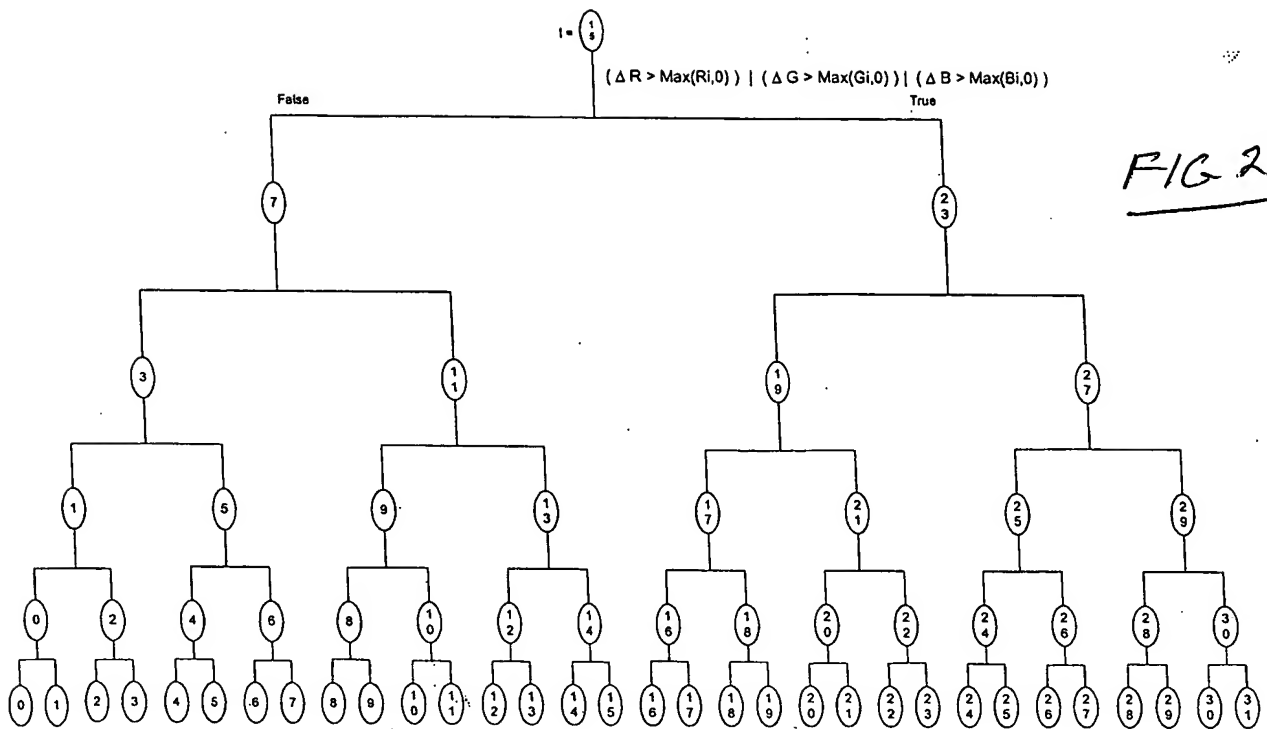


FIG 26





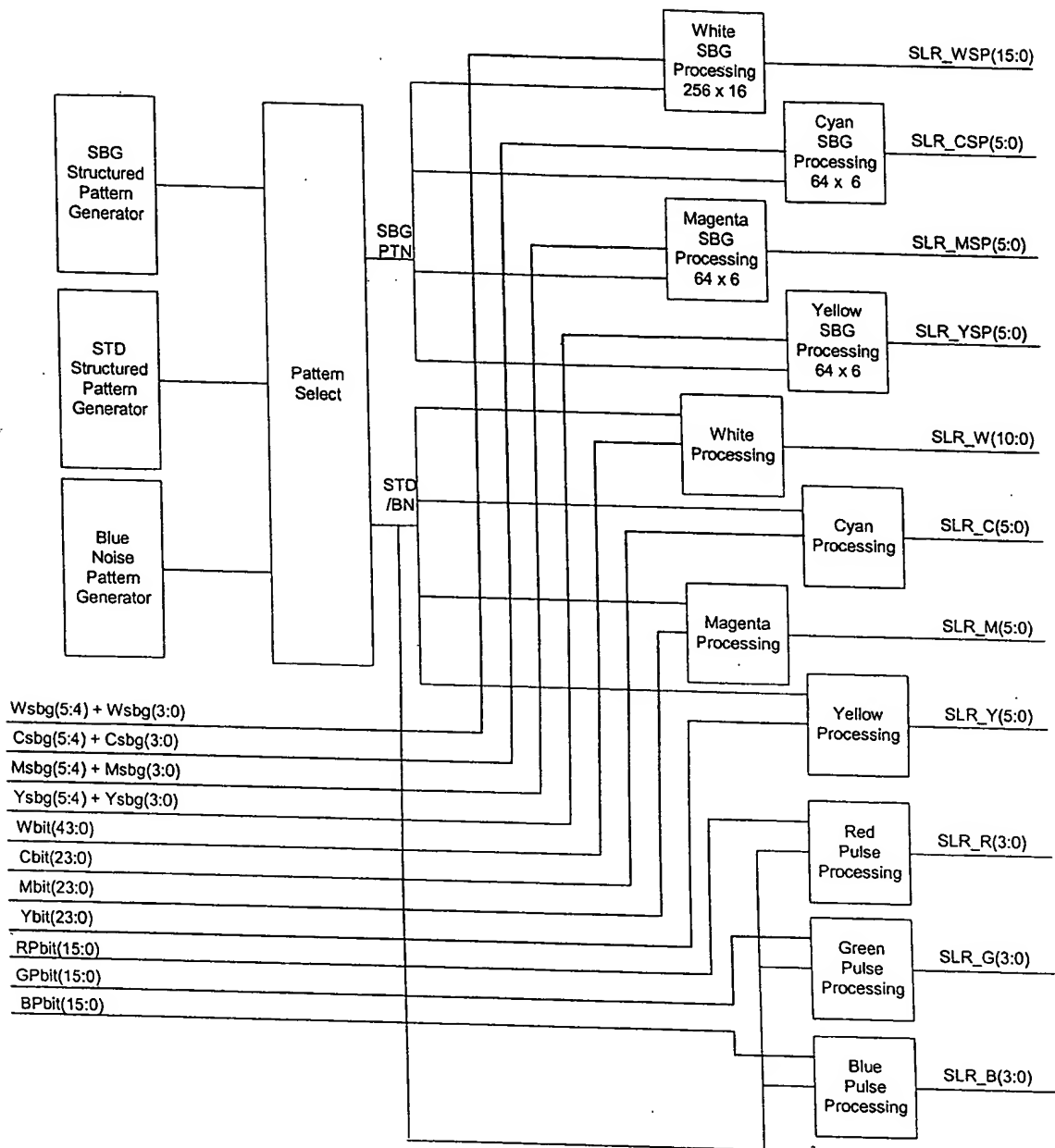


FIG 29